

Figure 1 (Prior Art)

New U.S. Patent Application
 Inventors: Franki Ngai Kit POON et al.
 Title: DIMMABLE BALLAST WITH RESISTIVE INPUT
 AND LOW ELECTROMAGNETIC INTERFERENCE
 Attorney Docket No.: V0690.0016

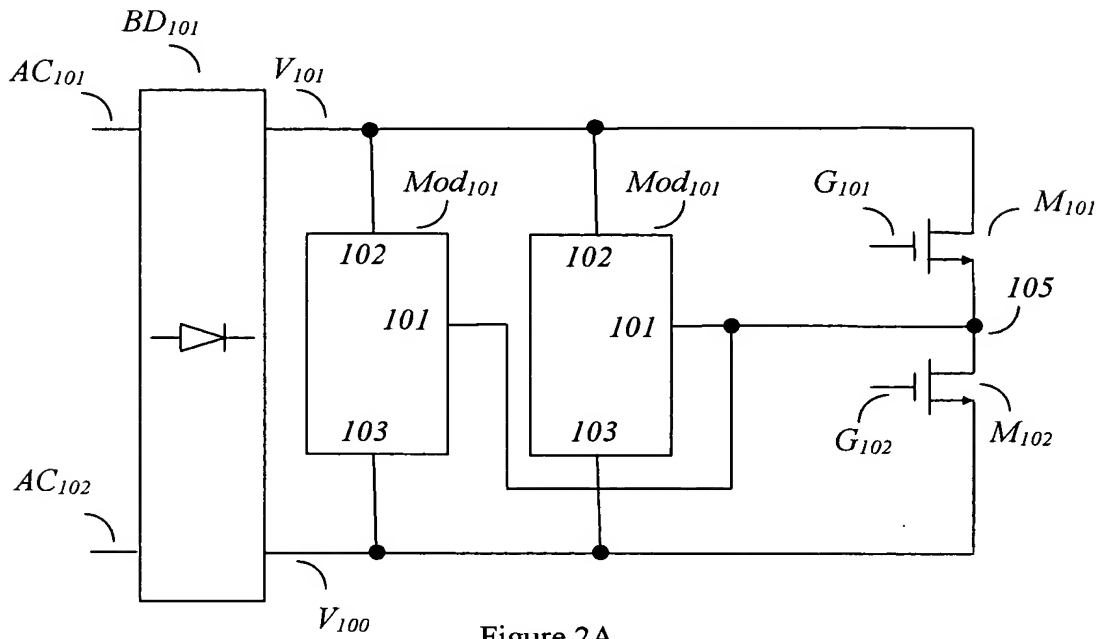


Figure 2A

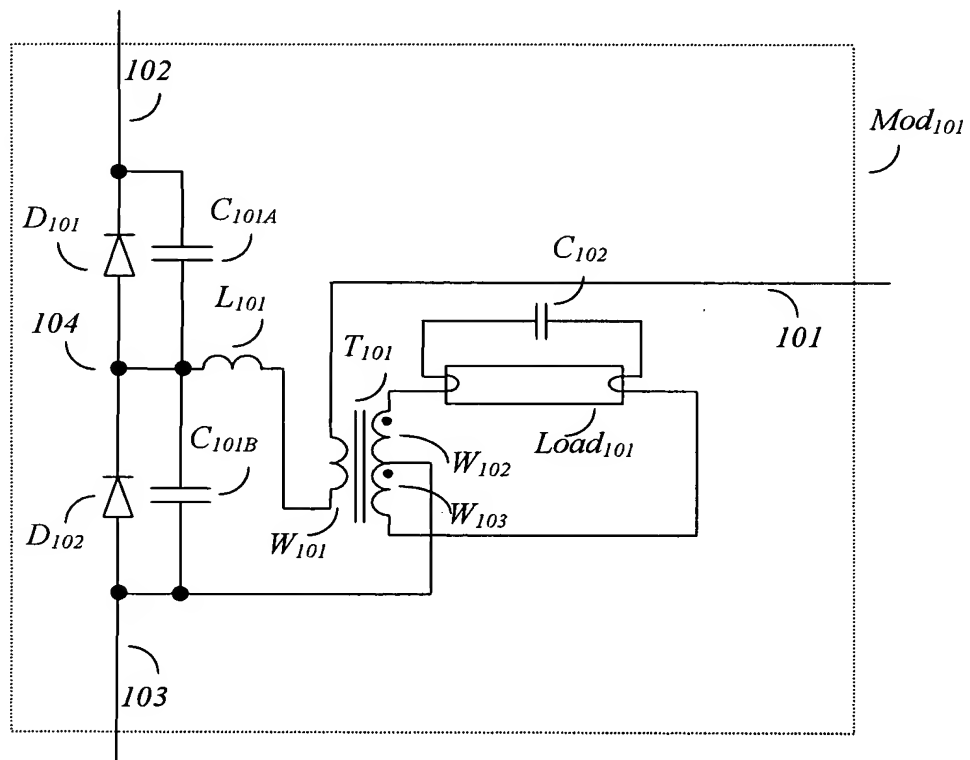


Figure 2B

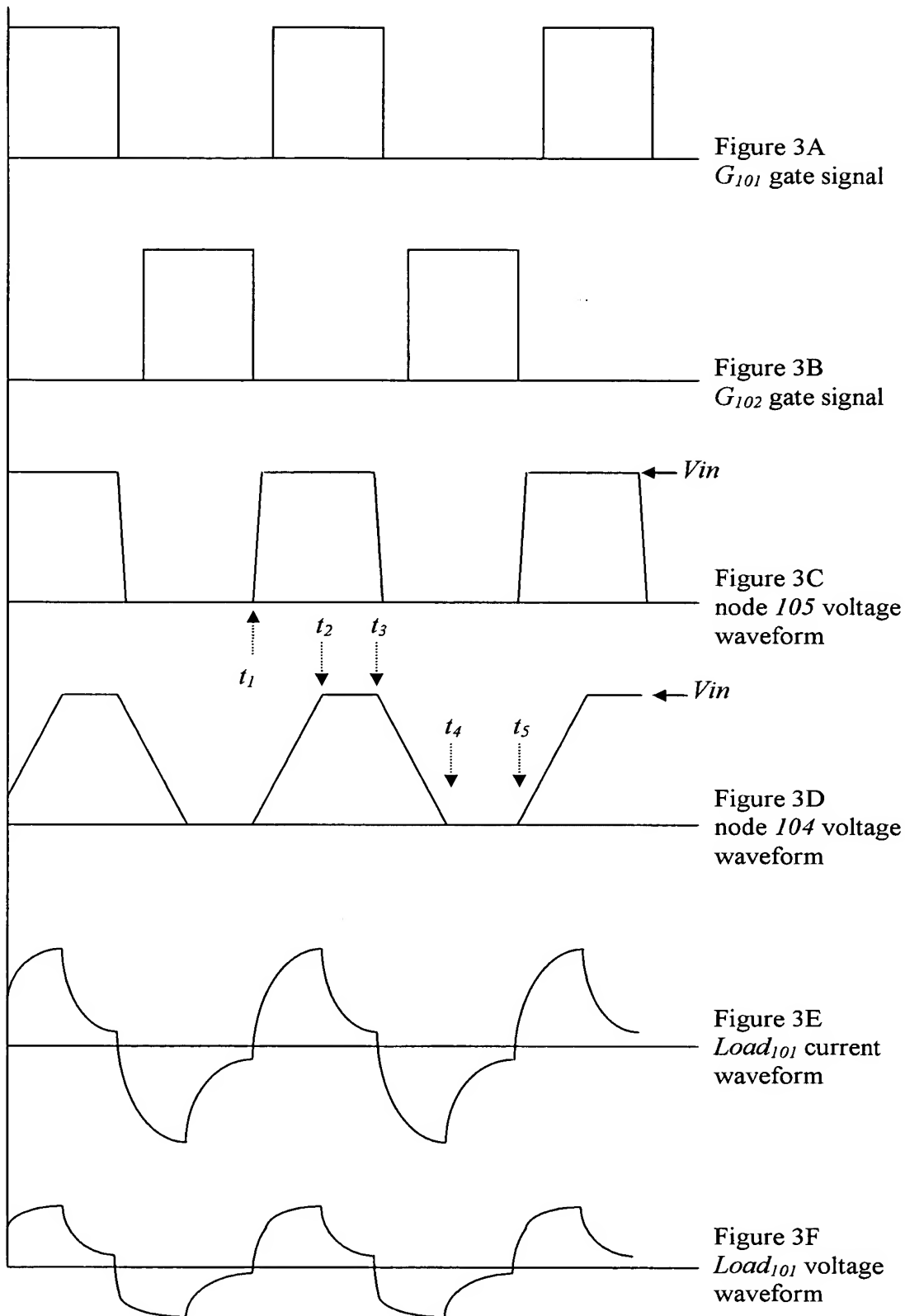


Figure 3

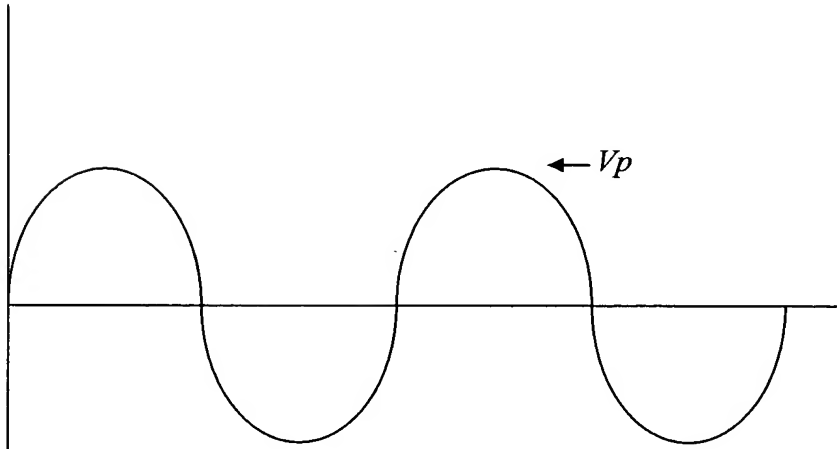


Figure 4A
Input AC voltage
waveform across
 AC_{101} and AC_{102}

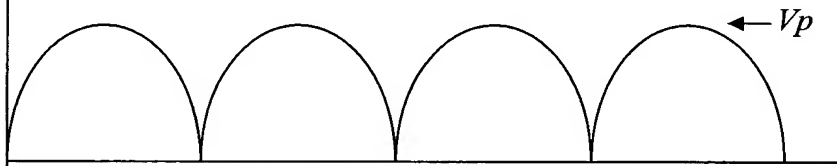


Figure 4B
Input rectified
voltage waveform
across V_{101} and V_{100}

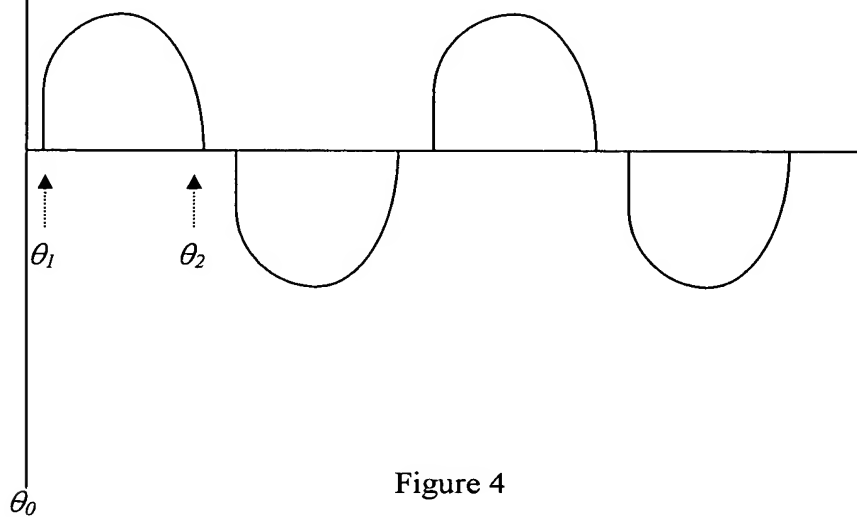


Figure 4C
Input current
waveform

Figure 4